LUTs for combinational functions (total\_luts): 1009

Non I/O Registers (non\_io\_reg): 334

I/O Pins: 26

I/O registers (total\_io\_reg): 0

DSP Blocks (dsp\_used): 0 (15)

Memory Bits: 30720

Clock: 147.0 MHz

147000000 \* 4 packets/8170 cycles = 71970 packets/second

Diagram, schematic

Description automatically generated

internal buffer FIFO:

A picture containing text, window

Description automatically generated